

TYPICAL LED FROM 7-SEGMENT DISPLAY.

TYPICAL SW INPUT.

8-PIN HEADER HDR1

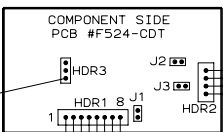
MASTER/SLAVE JUMPER
IN = MASTER
OUT = SLAVE

SPARE I/O (PAD ON PCB)

SPARE I/O (PAD ON PCB)

LAMP TEST SW

4-PIN HEADER HDR2



CONNECTOR PINOUTS AND JUMPER PLACEMENT DIAGRAM.

GND
GND
+5
RA0
RA1
RA2
RA3
RA4

REF:
SYRACUSE TIME AND ALARM - JAMESVILLE
SO. 23797 AFD-F524

AD AUTOMATION DISPLAYS INC.		
PIC COUNTDOWN TIMER SCHEMATIC		
A	DRAWN BY: TN	DWG # CTIMER
	DATE: 30 SEP 99	SHEET 1 OF 1

NOTES: